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Sheet	1	of	2	Attorney Docket Number	SC11522TS

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Examiner Signature	Date Considered	11-AUGUST-2003
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Application Number

Filing Date

First Named Inventor

Andrew K. Martin

Group Art Unit

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Narayanan Krishnamurthy et al., "Design and Development Paradigm for Industrial Formal Verification CAD Tools", IEEE Design & Test of Computers, July-August 2001, pgs. 26-35.

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J.R. Burch et al., "Representing Circuits More Efficiently in Symbolic Model Checking", 1991 28th ACM/IEEE Design Automation Conference, Paper 24.3, pgs. 403-407. /

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